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Mouish

843.39542X00



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: MINAMI et al

Serial No.: 09/768,471

Filed: January 25, 2001

For: SEMICONDUCTOR INTEGRATED CIRCUIT
DEVICE AND METHOD OF MANUFACTURING THE
SAME

Group: 2815

Examiner: B. W. BAUMEISTER

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RESPONSE TO ELECTION REQUIREMENT

Assistant Commissioner for Patents April 22, 2002
Washington, D.C. 20231

Sir:

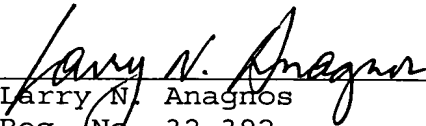
In the matter of the above-referenced referenced application, the following responsive remarks to the outstanding Office Action, dated March 21, 2002, are respectfully submitted. (The above-dated Office Action, it is noted, is a substitute for the earlier made Election Requirement of January 9, 2002, in which a response thereto was filed in the USPTO on February 11, 2002, and is a supplement to the originally made restriction requirement of September 18, 2001, in which an election was made in response thereto of the claims directed to the semiconductor device.) That is, the presently outstanding Office Action, dated March 21, 2002, calls for the further subdivision of the claims directed to the semiconductor

device into four (4) groups, as set forth in item 2 on page two thereof.

Regarding the presently outstanding restriction requirement, as detailed beginning on page two, item 2, of the Office Action, applicants hereby elect the invention according to Group IA, including claims 1-3 and 22, which are drawn to a Zener diode wherein the contact holes are formed at the type-II conductivity region periphery, without traverse.

To the extent necessary, applicants petition for an extension of time under 37 CFR §1.136. Please charge any shortage in the fees due in connection with the filing of this paper, to the Deposit Account of Antonelli, Terry, Stout & Kraus, LLP, Dep. Acct. No. 01-2135 (843.39542X00), and please credit any excess fees to such deposit account.

Respectfully submitted,
ANTONELLI, TERRY, STOUT & KRAUS, LLP



Larry N. Anagnos
Reg. No. 32,392

LNA/dks
(703) 312-6600